Amendments to the Specification

Please replace the paragraph starting at page 12, line 26 and ending at page 14, line 11, with the following rewritten paragraph:

-- A driver 23' is ~~arrange~~ <u>arranged</u> to drive a stepping motor 24 according to a signal produced from the CPU 40 for the purpose of controlling the position of the heads 3-1 and 3-2. The stepping motor 24 is thus arranged to shift the position of the heads 3-1 and 3-2 via the above stated head shifting device 4. A display circuit 25 is arranged to be driven by a signal from the CPU 40. The display circuit 25 is composed of display elements including, as shown in Fig. 3, seven-segment display elements which are arranged to display a number assigned to the track to which the head 3-1 has access in a two-place number and a speed at which the head is being shifted; a PB (play back) LED arranged to display a reproduction mode; a REC (recording) LED arranged to display a recording mode; a FRAME LED arranged to display a frame mode; and a FIELD LED arranged to display a field mode. A ROM 26 stores the program of the CPU 40. A RAM 27 is arranged to temporarily store the data of the CPU 40. A timer 28 is arranged to be driven by the CPU 40. A crystal oscillator 29 is arranged to generate reference clock pulses for the CPU 40. A detecting circuit 80 is connected to photo-couplers 81 and 82 which form a detection switch for detecting whether the magnetic sheet 1 is inserted in the apparatus. Elements 83 and 84 and a switch SW6 are provided for displaying at the monitor 13 and ~~a~~ <u>the</u> printer 13' a data signal (hereinafter referred to as ID signal) indicative of information on the year, the month, the day, etc. set as desired by the operator. More specifically, a synchronizing signal separation circuit 83 is arranged similarly to the synchronizing signal separation circuit 17 to ~~separte~~ <u>separate</u> synchronizing signals Vsync and Hsync from the video signal coming via a switch SW5 which is provided for adjustment of the timing of a data character to be generated. A character

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generator 84 is arranged to generate a character corresponding to data in synchronism with the synchronizing signals Vsync and Hsync separated by the circuit 83. In case where the ID signal is to be displayed in a state of being superimposed on the video signal at the monitor 13 or the printer 13', the CPU 40 produces a control signal to turn on the switch SW6. Then an adder 86 applies the synchronizing signals and the ID signal to the video signal to have the character displayed in a specific position on the monitor 13 or the printer 13'. --.

Please replace the paragraph starting at page 14, line 12 and ending at page 14, line 23, with the following rewritten paragraph:

-- An erase signal generator 85 is arranged to erase a signal recorded in any desired track on the magnetic sheet 1. An AC current to be used for ~~erasion~~ erasure is obtained from a reference signal generator 19. The erase signal generator 85 generates an attenuation signal which is composed of, for example, a constant amplitude period T1 and an ensuing attenuating period T2 as shown in Fig. 32. The circuit 85 is connected to the recording amplifier 16. The wave form of the attenuation signal is arranged to be suitable for magnetic recording medium. However, any other suitable wave form is of course usable even if the medium is a magnetic recording medium. --.

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